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December 2012

# FST3245 — 8-Bit Bus Switch

#### **Features**

- 4 Ω Switch Connection between Two Ports
- Minimal Propagation Delay through the Switch
- Low I<sub>CC</sub>
- Zero Bounce in Flow-through Mode
- Control Inputs Compatible with TTL Level

### **Description**

The FST3245 switch provides eight-bits of high-speed CMOS TTL-compatible bus switching in a standard '245 pin-out. The low on resistance allows inputs to be connected to outputs without adding propagation delay or generating additional ground bounce noise.

The device is organized as an eight-bit switch. When /OE is LOW, the switch is ON and port A is connected to port B. When /OE is HIGH, the switch is OPEN and a high-impedance state exists between the two ports.

### **Ordering Information**

Part Number	Operating Temperature Range	Package	Packing Method
FST3245MTCX	-40 to +85°C	20-Lead Thin Shrink Small Outline Package (TSSOP), JEDEC MO-153, 4.4 mm Wide	Tape and Reel

# **Logic Diagram**

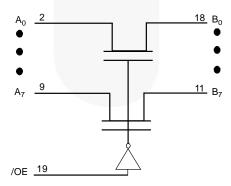


Figure 1. Logic Diagram

# **Pin Configuration**

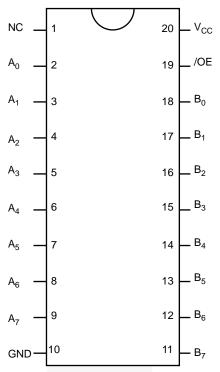


Figure 2. Pin Configuration

# **Pin Descriptions**

Pin #	Pin Names	Description
1	NC	No Connnect
19	/OE	Bus Switch Enable
2,3,4,5,6,7,8,9	$A_0, A_1, A_2, A_3, A_4, A_5, A_6, A_7$	Bus A
10	GND	Ground
11,12,13,14,15,16,17,18	B <sub>7</sub> ,B <sub>6</sub> ,B <sub>5</sub> ,B <sub>4</sub> ,B <sub>3</sub> ,B <sub>2</sub> ,B <sub>1</sub> ,B <sub>0</sub>	Bus B
20	V <sub>CC</sub>	Supply Voltage

## **Truth Table**

Input /OE	Function
LOW	Connect
HIGH	Disconnect

### **Absolute Maximum Ratings**

Stresses exceeding the absolute maximum ratings may damage the device. The device may not function or be operable above the recommended operating conditions and stressing the parts to these levels is not recommended. In addition, extended exposure to stresses above the recommended operating conditions may affect device reliability. The absolute maximum ratings are stress ratings only.

Symbol	Parameter	Min.	Max.	Unit
V <sub>CC</sub>	Supply Voltage	-0.5	7.0	V
Vs	DC Switch Voltage	-0.5	7.0	V
V <sub>IN</sub>	DC Input Voltage <sup>(1)</sup>	-0.5	7.0	V
I <sub>IK</sub>	DC Input Diode Current, V <sub>IN</sub> < 0 V		-50	mA
I <sub>OUT</sub>	DC Output Sink Current		128	mA
I <sub>CC</sub> / I <sub>GND</sub>	DC V <sub>CC</sub> / GND Current		±100	mA
T <sub>STG</sub>	Storage Temperature Range	-65	+150	°C

#### Note:

 The input and output negative voltage ratings may be exceeded if the input and output diode current ratings are observed.

### **Recommended Operating Conditions**

The Recommended Operating Conditions table defines the conditions for actual device operation. Recommended operating conditions are specified to ensure optimal performance to the datasheet specifications. Fairchild does not recommend exceeding them or designing to Absolute Maximum Ratings.

Symbol	Parar	Min.	Max.	Unit		
V <sub>CC</sub>	Power Supply Operating	4.0	5.5	V		
V <sub>IN</sub>	Input Voltage		0	5.5	V	
V <sub>OUT</sub>	Output Voltage		0	5.5	V	
	Innut Dice and Fall Time	Switch Control Input <sup>(2)</sup>	0	5	ns/V	
t <sub>r</sub> , t <sub>f</sub>	Input Rise and Fall Time Switch I/O		0	DC	115/ V	
T <sub>A</sub>	T <sub>A</sub> Operating Temperature, Free Air			+85	°C	

#### Note:

2. Unused control inputs must be held HIGH or LOW. They may not float.

#### **DC Electrical Characteristics**

Typical values are at  $V_{CC} = 5.0 \text{ V}$  and  $T_A = 25^{\circ}\text{C}$ .

Complete	Doromotor	Conditions	V 00	T <sub>A</sub> =-40 to +85°C			I linita
Symbol	Parameter	Parameter Conditions V <sub>CC</sub> (V)		Min.	Тур.	Max.	Units
V <sub>IK</sub>	Clamp Diode Voltage	I <sub>IN</sub> = -18 mA	4.5			-1.2	V
V <sub>IH</sub>	High-Level Input Voltage		4.0 to 5.5	2.0			٧
V <sub>IL</sub>	Low-Level Input Voltage		4.0 to 5.5			0.8	V
I <sub>IN</sub>	Input Leakage Current	$0 \le V_{IN} \le 5.5 \text{ V}$	5.5			±1.0	μΑ
I <sub>OZ</sub>	Off-state Leakage Current	$0 \le A, B \le V_{CC}$	5.5			±1.0	μΑ
		$V_{IN} = 0 \text{ V}, I_{IN} = 64 \text{ mA}$	4.5		4	7	
R <sub>on</sub>	Switch On Resistance <sup>(3)</sup>	$V_{IN} = 0 \text{ V}, I_{IN} = 30 \text{ mA}$	4.5		4	7	Ω
NON	Switch Off Resistance	V <sub>IN</sub> = 2.4 V, I <sub>IN</sub> = 15 mA	4.5		8	15	52
		$V_{IN} = 2.4 \text{ V}, I_{IN} = 15 \text{ mA}$	4.0		11	20	
I <sub>cc</sub>	Quiescent Supply Current	$V_{IN} = V_{CC}$ or GND, $I_{OUT} = 0$	5.5	(		3	μA
Δl <sub>CC</sub>	Increase in I <sub>CC</sub> per Input	One Input at 3.4 V, Other Inputs at V <sub>CC</sub> or GND	5.5			2.5	mA

#### Note:

3. Measured by the voltage drop between the A and B pins at the indicated current through the switch. On resistance is determined by the lower of the voltages on the A or B pins.

#### **AC Electrical Characteristics**

 $T_A$  = -40 to +85°C,  $C_L$  = 50 pF, and  $R_U$  =  $R_D$  = 500  $\Omega$ .

Symbol	Parameter	Conditions	$V_{CC} = 4.5$	– 5.5 V	V <sub>cc</sub> =	4.0 V	Units	Figure
Syllibol	Parameter		Min.	Max.	Min.	Max.	Units	Figure
t <sub>PHL</sub> , t <sub>PLH</sub>	Propagation Delay Bus-to-Bus <sup>(4)</sup>	V <sub>IN</sub> = Open		0.25		0.25	ns	Figure 3 Figure 4
t <sub>PZH</sub> ,t <sub>PZL</sub>	Output Enable Time	$V_{IN} = 7 \text{ V for } t_{PZL}$ $V_{IN} = \text{Open for } t_{PZH}$	1.5	5.9		6.4	ns	Figure 3 Figure 4
t <sub>PHZ</sub> , t <sub>PLZ</sub>	Output Disable Time	$V_{IN} = 7 \text{ V for } t_{PLZ}$ $V_{IN} = \text{Open for}$ $t_{PHZ}$	1.5	6.0		5.7	ns	Figure 3 Figure 4

#### Note:

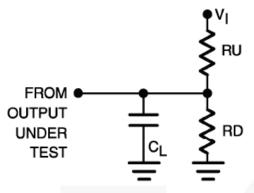
4. This parameter is guaranteed by design, but is not tested. The bus switch contributes no propagation delay other than the RC delay of the typical on resistance of the switch and the 50 pF load capacitance when driven by an ideal voltage source (zero output impedance).

### Capacitance

 $T_A = +25$ °C, f = 1 MHz. Capacitance is characterized, but not tested.

Symbol	Parameter	Conditions	Тур.	Units
C <sub>IN</sub>	Control Pin Input Capacitance	V <sub>CC</sub> = 5.0 V	3	pF
C <sub>I/O</sub>	Input/Output Capacitance	V <sub>CC</sub> , /OE = 5.0 V	5	pF

# **AC Loadings and Waveforms**



**Notes**: Input driven by 50  $\Omega$  source terminated in 50  $\Omega$ .  $C_L$  includes load and stray capacitance. Input PRR = 1.0 MHz,  $t_w$  = 500 ns.

Figure 3. AC Test Circuit

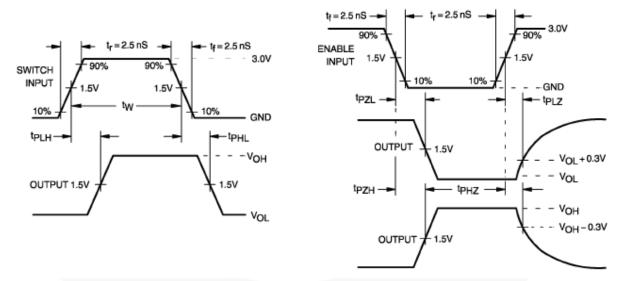
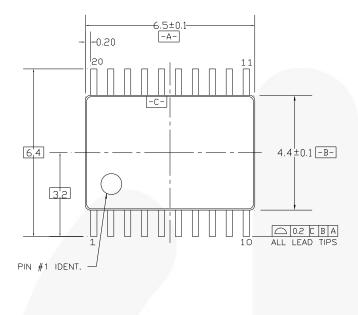
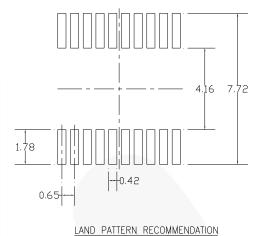
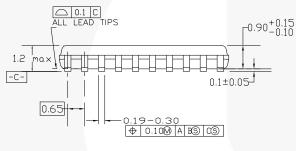


Figure 4. AC Waveforms

# **Physical Dimensions**



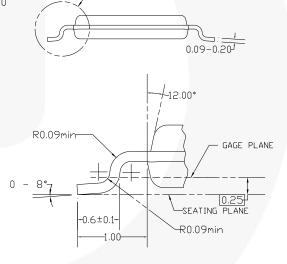




DIMENSIONS ARE IN MILLIMETERS

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- B. DIMENSIONS ARE IN MILLIMETERS.
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- D. DIMENSIONS AND TOLERANCES PER ANSI Y14.5M, 1982.



SEE DETAIL A

DETAIL A

MTC20REVD1

Figure 5. 20-Lead Thin Shrink Small Outline Package (TSSOP), JEDEC MO-153, 4.4 mm Wide

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