

Darlington Amplifier Transistors

NPN Silicon

MMBTA13L, SMMBTA13L, MMBTA14L, SMMBTA14L

Features

- S Prefix for Automotive and Other Applications Requiring Unique Site and Control Change Requirements; AEC-Q101 Qualified and PPAP Capable
- These Devices are Pb-Free, Halogen Free/BFR Free and are RoHS Compliant*

MAXIMUM RATINGS

Rating	Symbol	Value	Unit
Collector-Emitter Voltage	V_{CES}	30	Vdc
Collector-Base Voltage	V_{CBO}	30	Vdc
Emitter-Base Voltage	V_{EBO}	10	Vdc
Collector Current - Continuous	I_C	300	mAdc

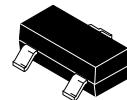
THERMAL CHARACTERISTICS

Characteristic	Symbol	Max	Unit
Total Device Dissipation FR-5 Board (Note 1) $T_A = 25^\circ\text{C}$ Derate above 25°C	P_D	225 1.8	mW mW/ $^\circ\text{C}$
Thermal Resistance, Junction-to-Ambient	$R_{\theta JA}$	556	$^\circ\text{C}/\text{W}$
Total Device Dissipation Alumina Substrate, (Note 2) $T_A = 25^\circ\text{C}$ Derate above 25°C	P_D	300 2.4	mW mW/ $^\circ\text{C}$
Thermal Resistance, Junction-to-Ambient	$R_{\theta JA}$	417	$^\circ\text{C}/\text{W}$
Junction and Storage Temperature	T_J, T_{stg}	-55 to +150	$^\circ\text{C}$

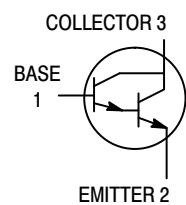
Stresses exceeding those listed in the Maximum Ratings table may damage the device. If any of these limits are exceeded, device functionality should not be assumed, damage may occur and reliability may be affected.

1. FR-5 = $1.0 \times 0.75 \times 0.062$ in.
2. Alumina = $0.4 \times 0.3 \times 0.024$ in. 99.5% alumina.

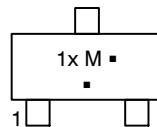
*For additional information on our Pb-Free strategy and soldering details, please download the **onsemi** Soldering and Mounting Techniques Reference Manual, SOLDERRM/D.



SOT-23 (TO-236)
CASE 318
STYLE 6



MARKING DIAGRAM



1x = Device Code
x = M for MMBTA13LT1G,
SMMBTA13LT1G
x = N for MMBTA14LT1G,
SMMBTA14LT1G, T3G
M = Date Code*
- = Pb-Free Package

(Note: Microdot may be in either location)

*Date Code orientation and/or overbar may vary depending upon manufacturing location.

ORDERING INFORMATION

Device	Package	Shipping [†]
MMBTA13LT1G, SMMBTA13LT1G	SOT-23 (Pb-Free)	3,000 / Tape & Reel
MMBTA14LT1G, SMMBTA14LT1G	SOT-23 (Pb-Free)	3,000 / Tape & Reel
SMMBTA14LT3G	SOT-23 (Pb-Free)	10,000 / Tape & Reel

[†]For information on tape and reel specifications, including part orientation and tape sizes, please refer to our Tape and Reel Packaging Specifications Brochure, BRD8011/D.

MMBTA13L, SMMBTA13L, MMBTA14L, SMMBTA14L

ELECTRICAL CHARACTERISTICS ($T_A = 25^\circ\text{C}$ unless otherwise noted)

Characteristic	Symbol	Min	Max	Unit
OFF CHARACTERISTICS				
Collector-Emitter Breakdown Voltage ($I_C = 100 \mu\text{A}\text{dc}$, $V_{BE} = 0$)	$V_{(BR)CES}$	30	–	Vdc
Collector Cutoff Current ($V_{CB} = 30 \text{ Vdc}$, $I_E = 0$)	I_{CBO}	–	100	nAdc
Emitter Cutoff Current ($V_{EB} = 10 \text{ Vdc}$, $I_C = 0$)	I_{EBO}	–	100	nAdc
ON CHARACTERISTICS (Note 3)				
DC Current Gain ($I_C = 10 \text{ mA}\text{dc}$, $V_{CE} = 5.0 \text{ Vdc}$) MMBTA13, SMMBTA13 MMBTA14, SMMBTA14 ($I_C = 100 \text{ mA}\text{dc}$, $V_{CE} = 5.0 \text{ Vdc}$) MMBTA13, SMMBTA13 MMBTA14, SMMBTA14	h_{FE}	5000 10,000 10,000 20,000	– – – –	–
Collector-Emitter Saturation Voltage ($I_C = 100 \text{ mA}\text{dc}$, $I_B = 0.1 \text{ mA}\text{dc}$)	$V_{CE(\text{sat})}$	–	1.5	Vdc
Base-Emitter On Voltage ($I_C = 100 \text{ mA}\text{dc}$, $V_{CE} = 5.0 \text{ Vdc}$)	V_{BE}	–	2.0	Vdc
SMALL-SIGNAL CHARACTERISTICS				
Current-Gain-Bandwidth Product (Note 4) ($I_C = 10 \text{ mA}\text{dc}$, $V_{CE} = 5.0 \text{ Vdc}$, $f = 100 \text{ MHz}$)	f_T	125	–	MHz

Product parametric performance is indicated in the Electrical Characteristics for the listed test conditions, unless otherwise noted. Product performance may not be indicated by the Electrical Characteristics if operated under different conditions.

3. Pulse Test: Pulse Width $\leq 300 \mu\text{s}$, Duty Cycle $\leq 2.0\%$.

4. $f_T = |h_{fe}| \cdot f_{\text{test}}$.

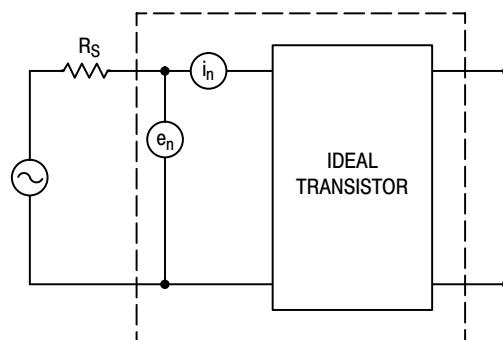


Figure 1. Transistor Noise Model

MMBTA13L, SMMBTA13L, MMBTA14L, SMMBTA14L

NOISE CHARACTERISTICS

($V_{CE} = 5.0$ VDC, $T_A = 25^\circ\text{C}$)

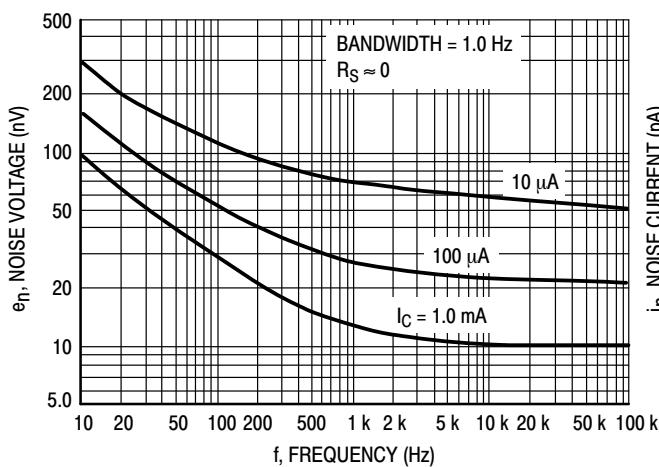


Figure 2. Noise Voltage

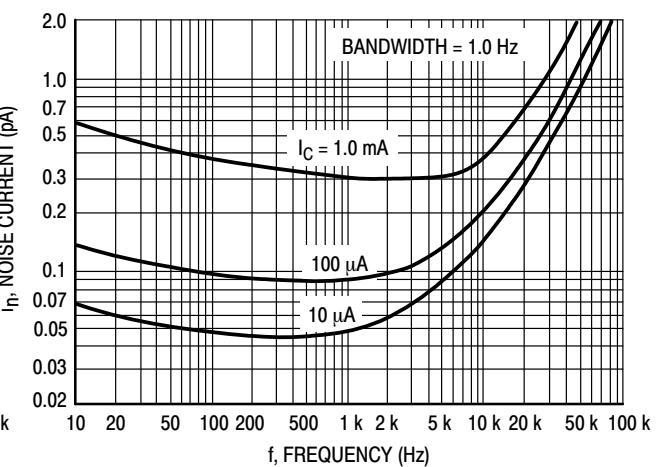


Figure 3. Noise Current

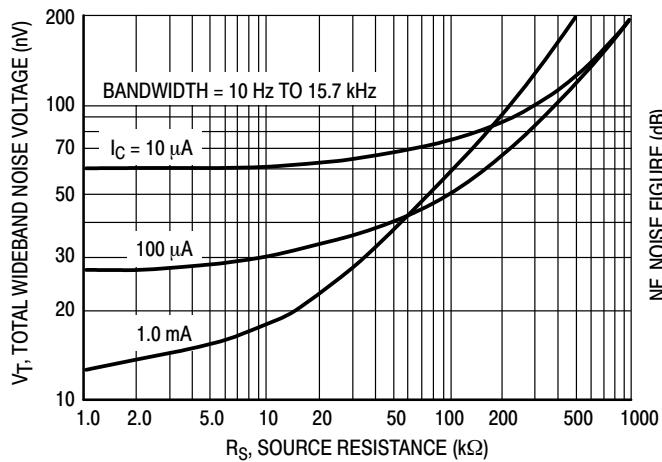


Figure 4. Total Wideband Noise Voltage

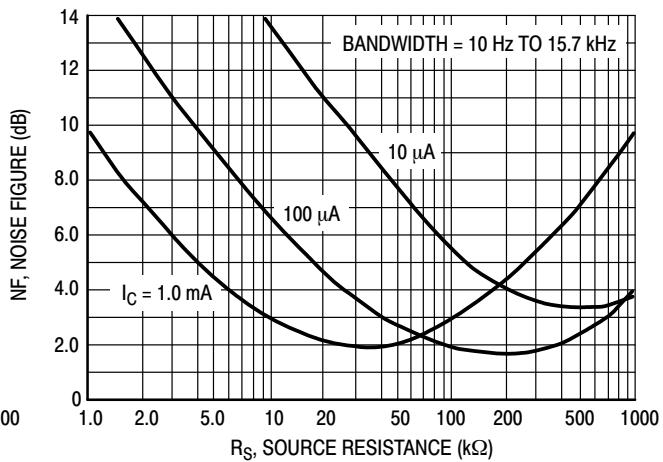
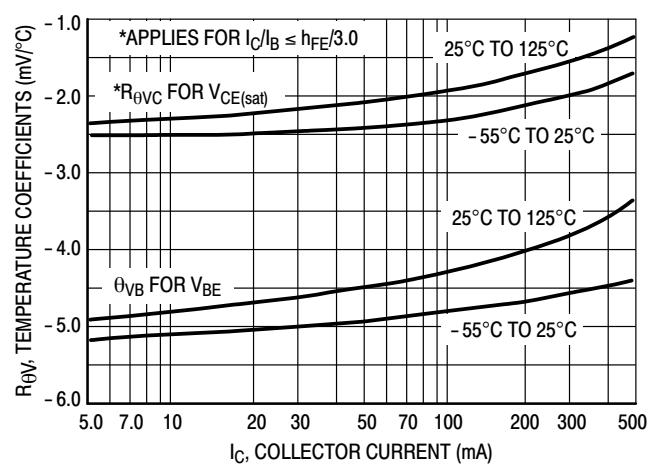
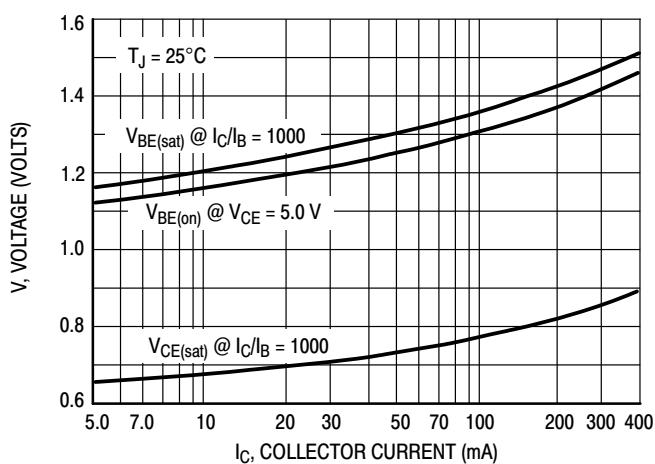
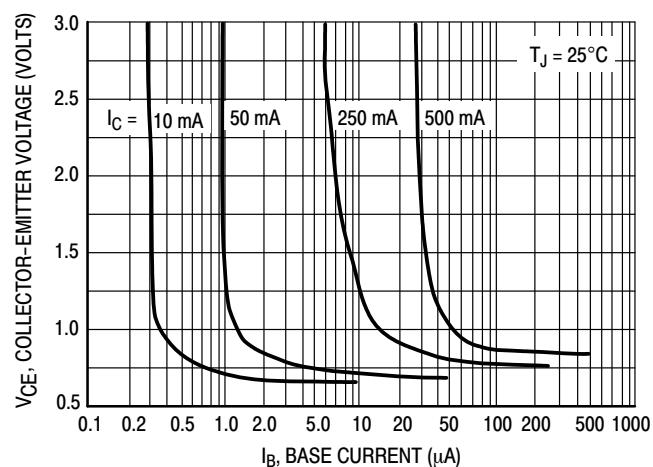
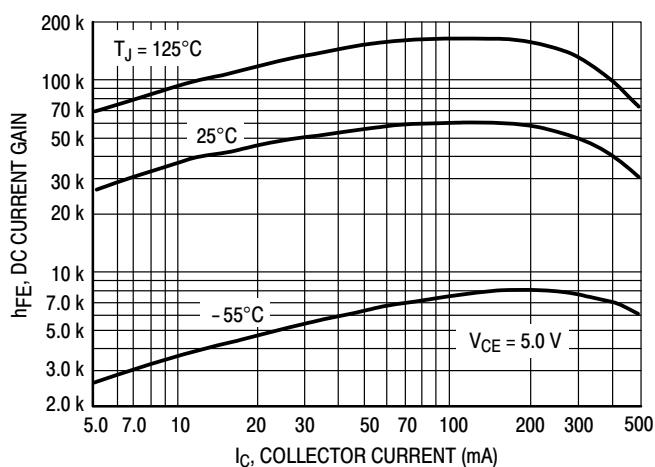
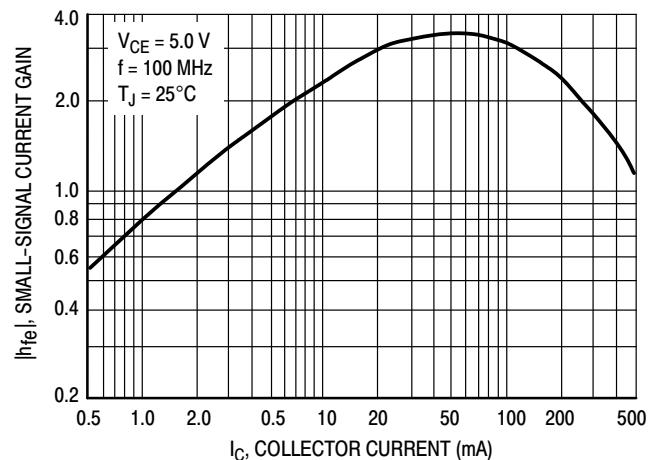
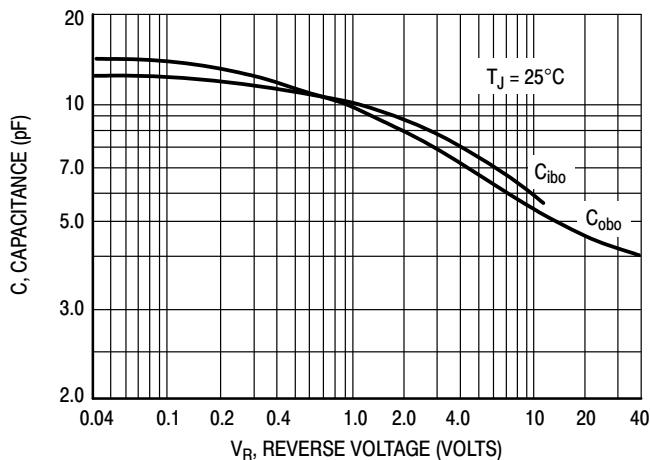


Figure 5. Wideband Noise Figure

MMBTA13L, SMMBTA13L, MMBTA14L, SMMBTA14L

SMALL-SIGNAL CHARACTERISTICS



MMBTA13L, SMMBTA13L, MMBTA14L, SMMBTA14L

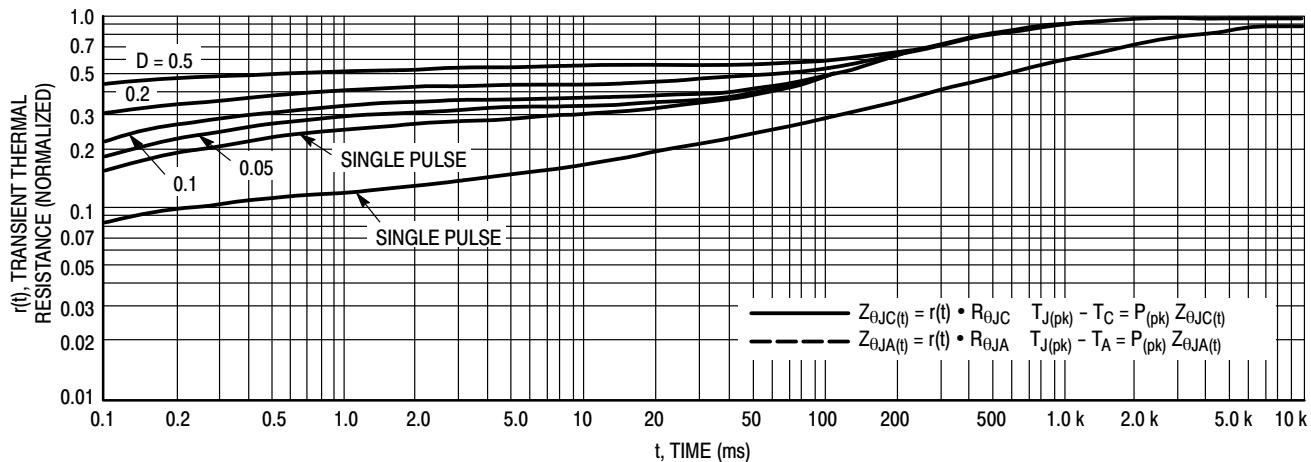


Figure 12. Thermal Response

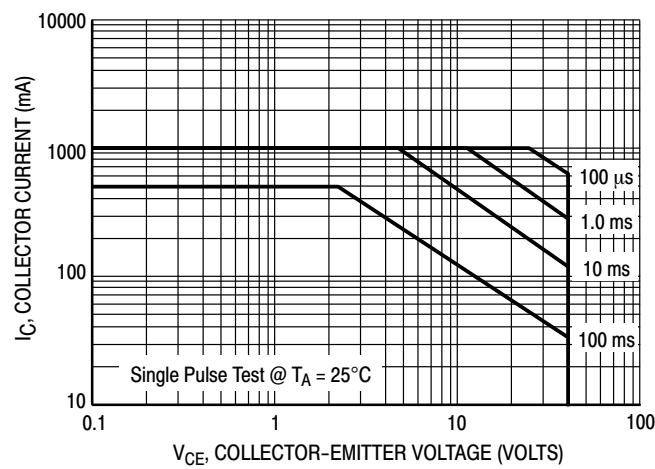
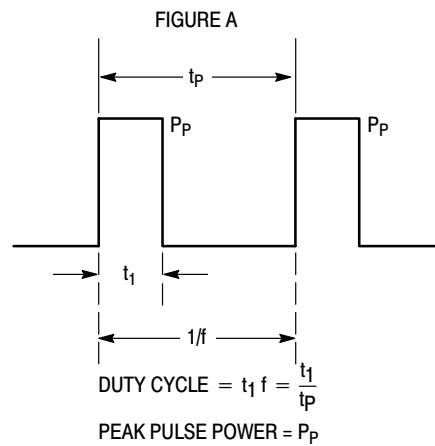
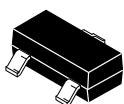


Figure 13. Active Region Safe Operating Area



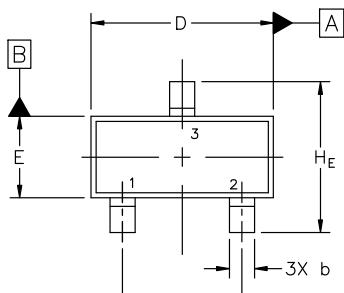
Design Note: Use of Transient Thermal Resistance Data



SCALE 4:1

SOT-23 (TO-236) 2.90x1.30x1.00 1.90P
CASE 318
ISSUE AU

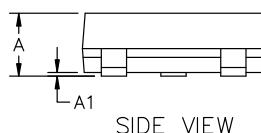
DATE 14 AUG 2024



TOP VIEW

DETAIL "A"

Scale 3:1

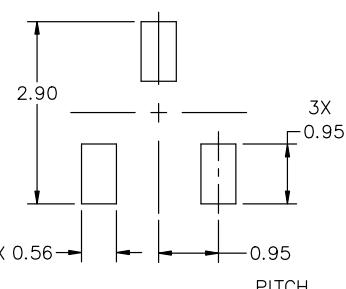


SIDE VIEW

DETAIL "A"

END VIEW

SEATING PLANE

GENERIC
MARKING DIAGRAM*

XXX = Specific Device Code
M = Date Code
■ = Pb-Free Package

*This information is generic. Please refer to device data sheet for actual part marking. Pb-Free indicator, "G" or microdot "■", may or may not be present. Some products may not follow the Generic Marking.

MILLIMETERS			
DIM	MIN	NOM	MAX
A	0.89	1.00	1.11
A1	0.01	0.06	0.10
b	0.37	0.44	0.50
c	0.08	0.14	0.20
D	2.80	2.90	3.04
E	1.20	1.30	1.40
e	1.78	1.90	2.04
L	0.30	0.43	0.55
L1	0.35	0.54	0.69
HE	2.10	2.40	2.64
T	0°	---	10°

NOTES:

1. DIMENSIONING AND TOLERANCING PER ASME Y14.5M, 2018.
2. CONTROLLING DIMENSIONS: MILLIMETERS.
3. MAXIMUM LEAD THICKNESS INCLUDES LEAD FINISH. MINIMUM LEAD THICKNESS IS THE MINIMUM THICKNESS OF THE BASE MATERIAL.
4. DIMENSIONS D AND E DO NOT INCLUDE MOLD FLASH, PROTRUSIONS, OR GATE BURRS.

RECOMMENDED
MOUNTING FOOTPRINT

* For additional information on our Pb-Free strategy and soldering details, please download the onsemi Soldering and Mounting Techniques Reference Manual, SOLDERRM/D.

STYLES ON PAGE 2

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CASE 318

ISSUE AU

DATE 14 AUG 2024

STYLE 1 THRU 5:
CANCELLEDSTYLE 6:
PIN 1. BASE
2. Emitter
3. CollectorSTYLE 7:
PIN 1. Emitter
2. Base
3. CollectorSTYLE 8:
PIN 1. Anode
2. No Connection
3. CathodeSTYLE 9:
PIN 1. Anode
2. Anode
3. CathodeSTYLE 10:
PIN 1. Drain
2. Source
3. GateSTYLE 11:
PIN 1. Anode
2. Cathode
3. Cathode-AnodeSTYLE 12:
PIN 1. Cathode
2. Cathode
3. AnodeSTYLE 13:
PIN 1. Source
2. Drain
3. GateSTYLE 14:
PIN 1. Cathode
2. Gate
3. AnodeSTYLE 15:
PIN 1. Gate
2. Cathode
3. AnodeSTYLE 16:
PIN 1. Anode
2. Cathode
3. CathodeSTYLE 17:
PIN 1. No Connection
2. Anode
3. CathodeSTYLE 18:
PIN 1. No Connection
2. Cathode
3. AnodeSTYLE 19:
PIN 1. Cathode
2. Anode
3. Cathode-AnodeSTYLE 20:
PIN 1. Cathode
2. Anode
3. GateSTYLE 21:
PIN 1. Gate
2. Source
3. DrainSTYLE 22:
PIN 1. Return
2. Output
3. InputSTYLE 23:
PIN 1. Anode
2. Anode
3. CathodeSTYLE 24:
PIN 1. Gate
2. Drain
3. SourceSTYLE 25:
PIN 1. Anode
2. Cathode
3. GateSTYLE 26:
PIN 1. Cathode
2. Anode
3. No ConnectionSTYLE 27:
PIN 1. Cathode
2. Cathode
3. CathodeSTYLE 28:
PIN 1. Anode
2. Anode
3. Anode

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