

**100 V, 3.0 A, Low $V_{CE(sat)}$
NPN Transistor****NSS1C201L, NSV1C201L**

onsemi's e²PowerEdge family of low $V_{CE(sat)}$ transistors are miniature surface mount devices featuring ultra low saturation voltage ($V_{CE(sat)}$) and high current gain capability. These are designed for use in low voltage, high speed switching applications where affordable efficient energy control is important.

Typical applications are DC-DC converters and power management in portable and battery powered products such as cellular and cordless phones, PDAs, computers, printers, digital cameras and MP3 players. Other applications are low voltage motor controls in mass storage products such as disc drives and tape drives. In the automotive industry they can be used in air bag deployment and in the instrument cluster. The high current gain allows e²PowerEdge devices to be driven directly from PMU's control outputs, and the Linear Gain (Beta) makes them ideal components in analog amplifiers.

Features

- NSV Prefix for Automotive and Other Applications Requiring Unique Site and Control Change Requirements; AEC-Q101 Qualified and PPAP Capable
- These Devices are Pb-Free, Halogen Free/BFR Free and are RoHS Compliant

MAXIMUM RATINGS (T_A = 25°C)

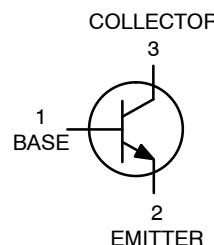
Rating	Symbol	Max	Unit
Collector-Emitter Voltage	V_{CEO}	100	Vdc
Collector-Base Voltage	V_{CBO}	140	Vdc
Emitter-Base Voltage	V_{EBO}	7.0	Vdc
Collector Current – Continuous	I_C	2.0	A
Collector Current – Peak	I_{CM}	3.0	A

THERMAL CHARACTERISTICS

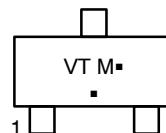
Characteristic	Symbol	Max	Unit
Total Device Dissipation $T_A = 25^\circ C$ Derate above 25°C	P_D (Note 1)	490 3.7	mW mW/°C
Thermal Resistance, Junction-to-Ambient	$R_{\theta JA}$ (Note 1)	255	°C/W
Total Device Dissipation $T_A = 25^\circ C$ Derate above 25°C	P_D (Note 2)	710 4.3	mW mW/°C
Thermal Resistance, Junction-to-Ambient	$R_{\theta JA}$ (Note 2)	176	°C/W
Junction and Storage Temperature Range	T_J, T_{stg}	-55 to +150	°C

Stresses exceeding those listed in the Maximum Ratings table may damage the device. If any of these limits are exceeded, device functionality should not be assumed, damage may occur and reliability may be affected.

1. FR-4 @ 100 mm², 1 oz. copper traces.
2. FR-4 @ 500 mm², 1 oz. copper traces.

**100 VOLTS, 3.0 AMPS
NPN LOW $V_{CE(sat)}$ TRANSISTOR**

**SOT-23 (TO-236)
CASE 318
STYLE 6**

MARKING DIAGRAM

VT = Specific Device Code

M = Date Code*

▪ = Pb-Free Package

(Note: Microdot may be in either location)

*Date Code orientation and/or overbar may vary depending upon manufacturing location.

ORDERING INFORMATION

Device	Package	Shipping [†]
NSS1C201LT1G, NSV1C201LT1G	SOT-23 (Pb-Free)	3000 / Tape & Reel

[†]For information on tape and reel specifications, including part orientation and tape sizes, please refer to our Tape and Reel Packaging Specification Brochure, BRD8011/D.

NSS1C201L, NSV1C201L

ELECTRICAL CHARACTERISTICS ($T_A = 25^\circ\text{C}$ unless otherwise noted)

Characteristic	Symbol	Min	Typ	Max	Unit
OFF CHARACTERISTICS					
Collector – Emitter Breakdown Voltage ($I_C = 10 \text{ mA}_\text{dc}$, $I_B = 0$)	$V_{(\text{BR})\text{CEO}}$	100			Vdc
Collector – Base Breakdown Voltage ($I_C = 0.1 \text{ mA}_\text{dc}$, $I_E = 0$)	$V_{(\text{BR})\text{CBO}}$	140			Vdc
Emitter – Base Breakdown Voltage ($I_E = 0.1 \text{ mA}_\text{dc}$, $I_C = 0$)	$V_{(\text{BR})\text{EBO}}$	7.0			Vdc
Collector Cutoff Current ($V_{CB} = 140 \text{ Vdc}$, $I_E = 0$)	I_{CBO}			100	nA _{dc}
Emitter Cutoff Current ($V_{EB} = 6.0 \text{ Vdc}$)	I_{EBO}			50	nA _{dc}
ON CHARACTERISTICS					
DC Current Gain (Note 3) ($I_C = 10 \text{ mA}$, $V_{CE} = 2.0 \text{ V}$) ($I_C = 500 \text{ mA}$, $V_{CE} = 2.0 \text{ V}$) ($I_C = 1.0 \text{ A}$, $V_{CE} = 2.0 \text{ V}$) ($I_C = 2.0 \text{ A}$, $V_{CE} = 2.0 \text{ V}$)	h_{FE}	150 120 80 40	240	360	
Collector – Emitter Saturation Voltage (Note 3) ($I_C = 0.1 \text{ A}$, $I_B = 0.01 \text{ A}$) ($I_C = 0.5 \text{ A}$, $I_B = 0.05 \text{ A}$) ($I_C = 1.0 \text{ A}$, $I_B = 0.100 \text{ A}$) ($I_C = 2.0 \text{ A}$, $I_B = 0.200 \text{ A}$)	$V_{CE(\text{sat})}$			0.030 0.060 0.090 0.150	V
Base – Emitter Saturation Voltage (Note 3) ($I_C = 1.0 \text{ A}$, $I_B = 0.100 \text{ A}$)	$V_{BE(\text{sat})}$			0.950	V
Base – Emitter Turn-on Voltage (Note 3) ($I_C = 1.0 \text{ A}$, $V_{CE} = 2.0 \text{ V}$)	$V_{BE(\text{on})}$			0.850	V
Cutoff Frequency ($I_C = 100 \text{ mA}$, $V_{CE} = 5.0 \text{ V}$, $f = 100 \text{ MHz}$)	f_T		110		MHz
Input Capacitance ($V_{EB} = 2.0 \text{ V}$, $f = 1.0 \text{ MHz}$)	C_{ib}		230		pF
Output Capacitance ($V_{CB} = 10 \text{ V}$, $f = 1.0 \text{ MHz}$)	C_{ob}		14		pF

Product parametric performance is indicated in the Electrical Characteristics for the listed test conditions, unless otherwise noted. Product performance may not be indicated by the Electrical Characteristics if operated under different conditions.

3. Pulsed Condition: Pulse Width = 300 msec, Duty Cycle $\leq 2\%$.

TYPICAL CHARACTERISTICS

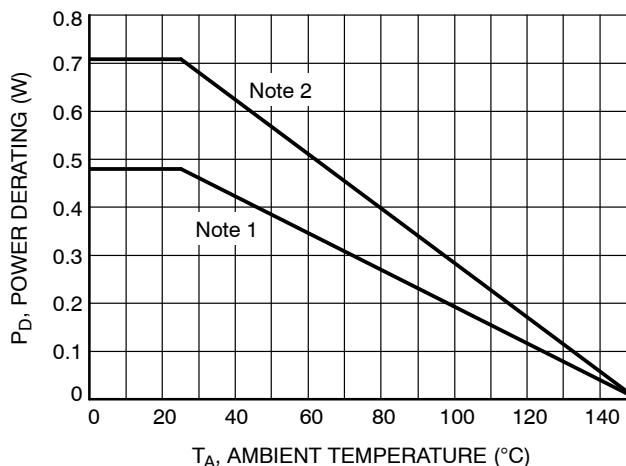


Figure 1. Power Derating

NSS1C201L, NSV1C201L

TYPICAL CHARACTERISTICS

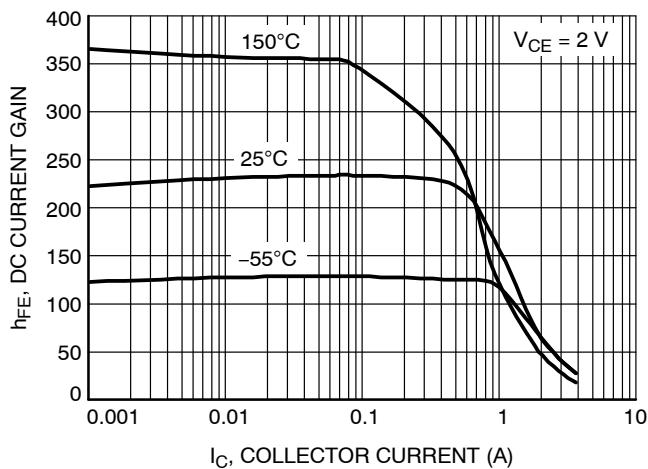


Figure 2. DC Current Gain

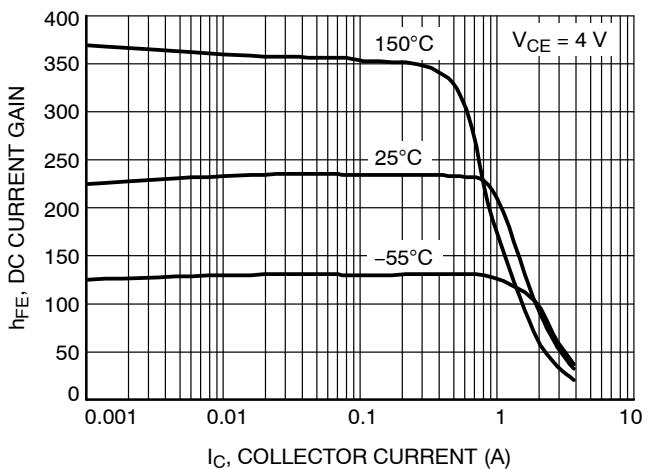


Figure 3. DC Current Gain

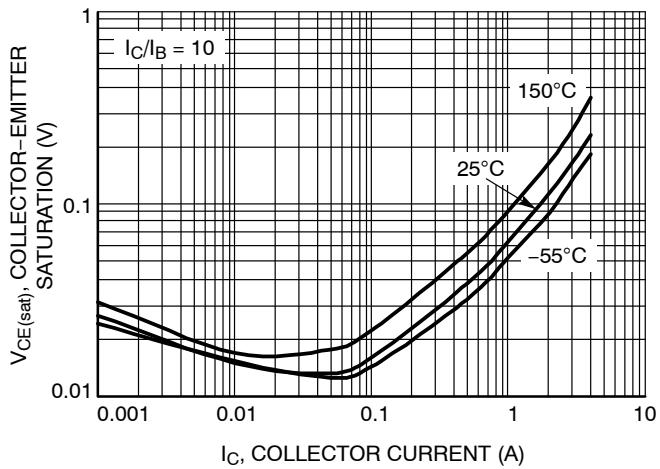


Figure 4. Collector-Emitter Saturation Voltage

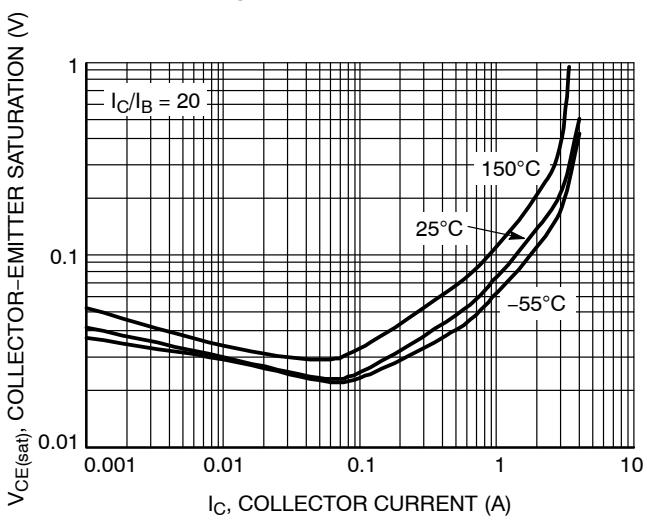


Figure 5. Collector-Emitter Saturation Voltage

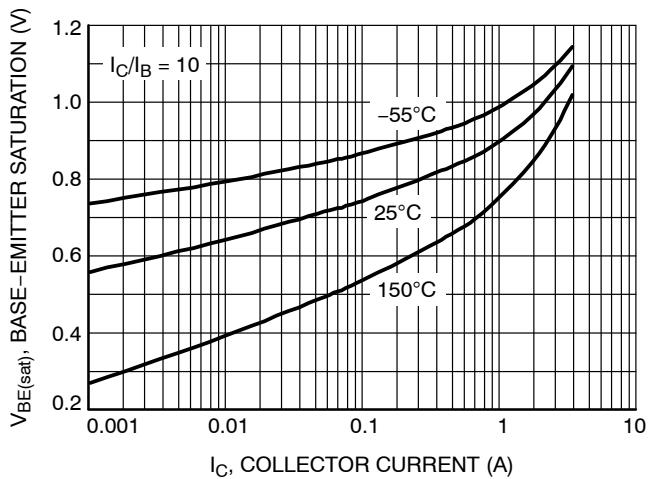


Figure 6. Base-Emitter Saturation Voltage

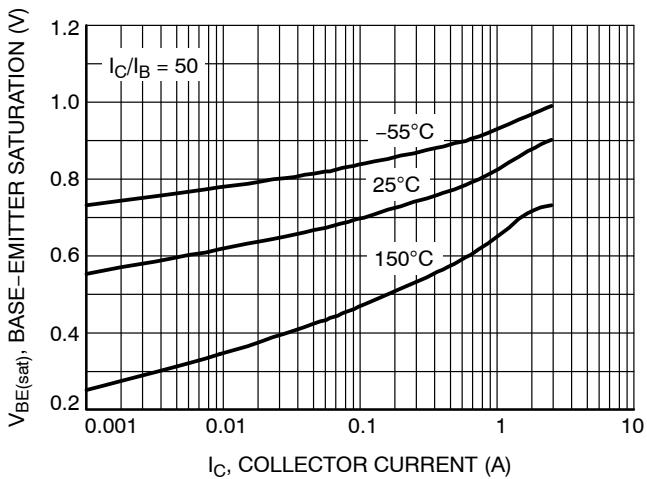


Figure 7. Base-Emitter Saturation Voltage

NSS1C201L, NSV1C201L

TYPICAL CHARACTERISTICS

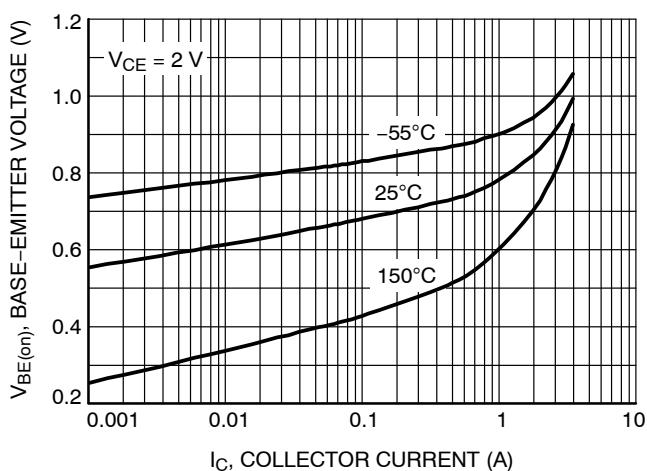


Figure 8. Base Emitter Voltage

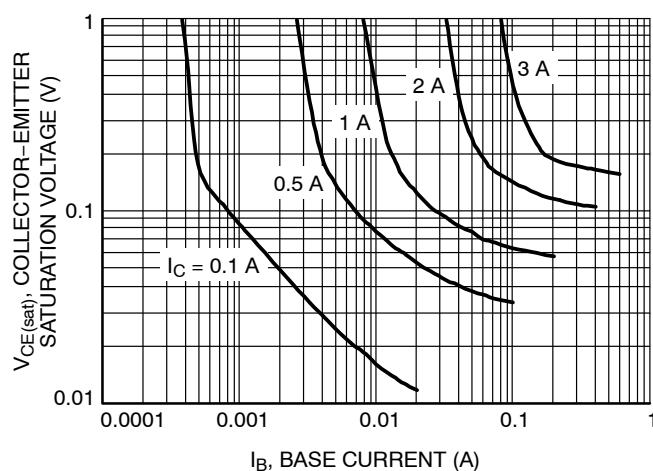


Figure 9. Collector Saturation Region

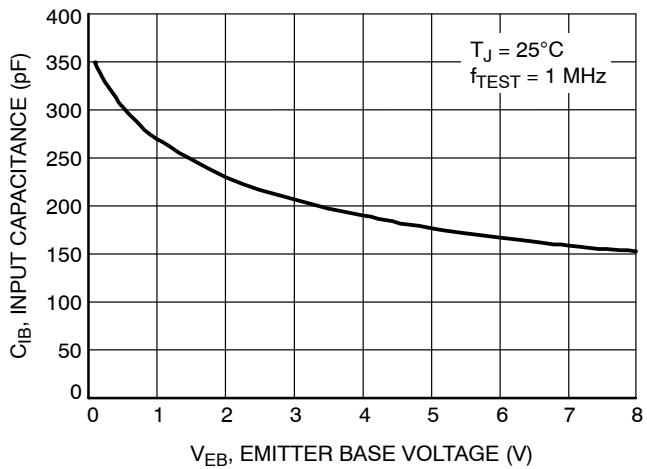


Figure 10. Input Capacitance

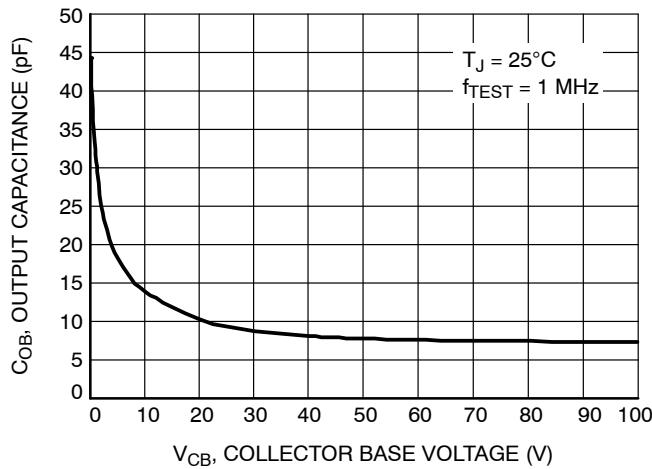


Figure 11. Output Capacitance

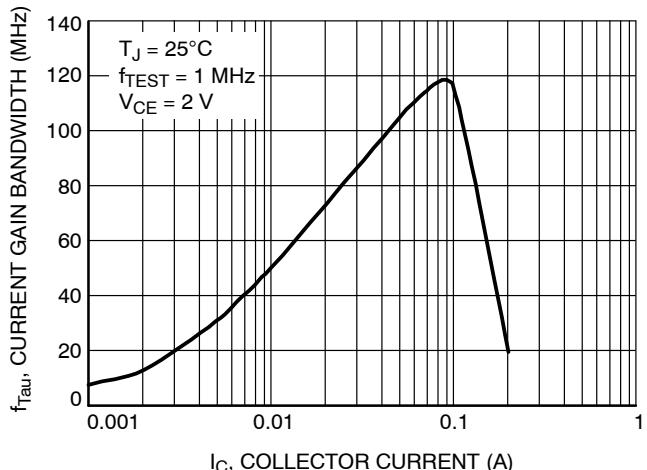


Figure 12. Current-Gain Bandwidth Product

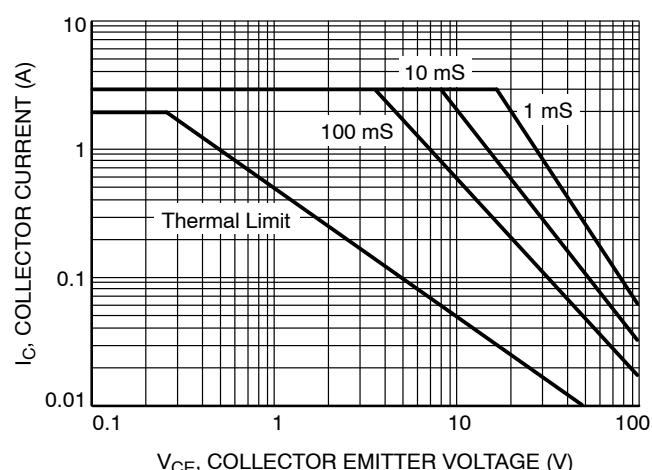


Figure 13. Safe Operating Area

NSS1C201L, NSV1C201L

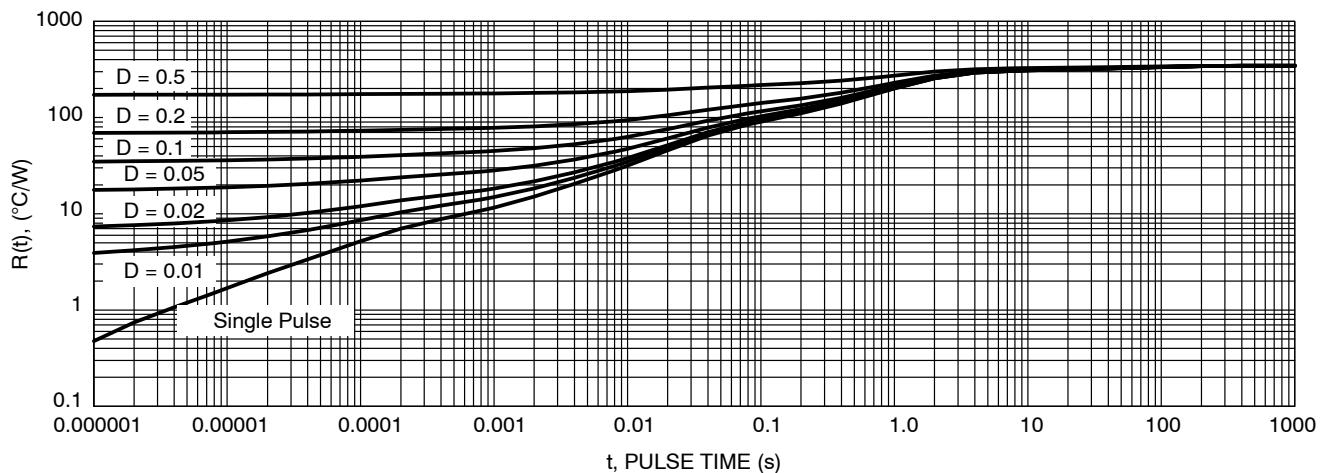
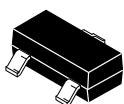


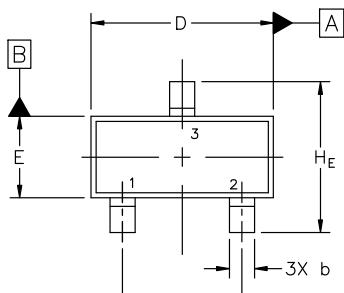
Figure 14. Transient Thermal Resistnce



SCALE 4:1

SOT-23 (TO-236) 2.90x1.30x1.00 1.90P
CASE 318
ISSUE AU

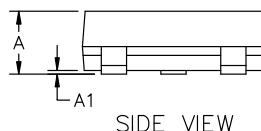
DATE 14 AUG 2024



TOP VIEW

DETAIL "A"

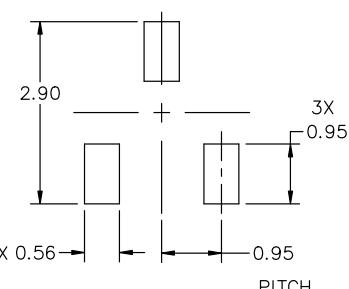
Scale 3:1



SIDE VIEW

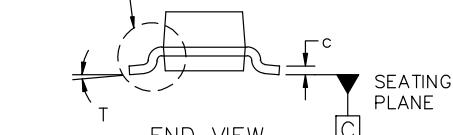
DETAIL "A"

END VIEW

SEATING PLANE
CGENERIC
MARKING DIAGRAM*

XXX = Specific Device Code
M = Date Code
■ = Pb-Free Package

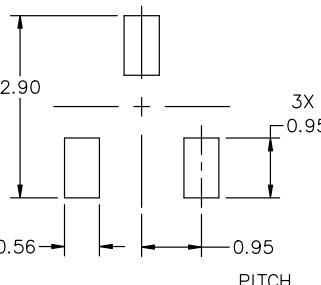
*This information is generic. Please refer to device data sheet for actual part marking. Pb-Free indicator, "G" or microdot "■", may or may not be present. Some products may not follow the Generic Marking.



MILLIMETERS			
DIM	MIN	NOM	MAX
A	0.89	1.00	1.11
A1	0.01	0.06	0.10
b	0.37	0.44	0.50
c	0.08	0.14	0.20
D	2.80	2.90	3.04
E	1.20	1.30	1.40
e	1.78	1.90	2.04
L	0.30	0.43	0.55
L1	0.35	0.54	0.69
HE	2.10	2.40	2.64
T	0°	---	10°

NOTES:

1. DIMENSIONING AND TOLERANCING PER ASME Y14.5M, 2018.
2. CONTROLLING DIMENSIONS: MILLIMETERS.
3. MAXIMUM LEAD THICKNESS INCLUDES LEAD FINISH. MINIMUM LEAD THICKNESS IS THE MINIMUM THICKNESS OF THE BASE MATERIAL.
4. DIMENSIONS D AND E DO NOT INCLUDE MOLD FLASH, PROTRUSIONS, OR GATE BURRS.

RECOMMENDED
MOUNTING FOOTPRINT

* For additional information on our Pb-Free strategy and soldering details, please download the onsemi Soldering and Mounting Techniques Reference Manual, SOLDERRM/D.

STYLES ON PAGE 2

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SOT-23 (TO-236) 2.90x1.30x1.00 1.90P

CASE 318

ISSUE AU

DATE 14 AUG 2024

STYLE 1 THRU 5:
CANCELLEDSTYLE 6:
PIN 1. BASE
2. Emitter
3. CollectorSTYLE 7:
PIN 1. Emitter
2. Base
3. CollectorSTYLE 8:
PIN 1. Anode
2. No Connection
3. CathodeSTYLE 9:
PIN 1. Anode
2. Anode
3. CathodeSTYLE 10:
PIN 1. Drain
2. Source
3. GateSTYLE 11:
PIN 1. Anode
2. Cathode
3. Cathode-AnodeSTYLE 12:
PIN 1. Cathode
2. Cathode
3. AnodeSTYLE 13:
PIN 1. Source
2. Drain
3. GateSTYLE 14:
PIN 1. Cathode
2. Gate
3. AnodeSTYLE 15:
PIN 1. Gate
2. Cathode
3. AnodeSTYLE 16:
PIN 1. Anode
2. Cathode
3. CathodeSTYLE 17:
PIN 1. No Connection
2. Anode
3. CathodeSTYLE 18:
PIN 1. No Connection
2. Cathode
3. AnodeSTYLE 19:
PIN 1. Cathode
2. Anode
3. Cathode-AnodeSTYLE 20:
PIN 1. Cathode
2. Anode
3. GateSTYLE 21:
PIN 1. Gate
2. Source
3. DrainSTYLE 22:
PIN 1. Return
2. Output
3. InputSTYLE 23:
PIN 1. Anode
2. Anode
3. CathodeSTYLE 24:
PIN 1. Gate
2. Drain
3. SourceSTYLE 25:
PIN 1. Anode
2. Cathode
3. GateSTYLE 26:
PIN 1. Cathode
2. Anode
3. No ConnectionSTYLE 27:
PIN 1. Cathode
2. Cathode
3. CathodeSTYLE 28:
PIN 1. Anode
2. Anode
3. Anode

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