

General Purpose Phototransistor Optocouplers, 6-Pin

MCT2EM, TIL111M, TIL117M

Description

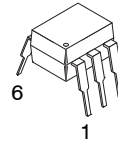
The general purpose optocouplers consist of a gallium arsenide infrared emitting diode driving a silicon photo-transistor in a standard plastic six-pin dual-in-line package.

Features

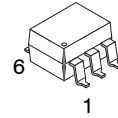
- Minimum Current Transfer Ratio at $I_F = 10 \text{ mA}$, $V_{CE} = 10 \text{ V}$
 - ◆ 20% for MCT2EM
 - ◆ 50% for TIL117M
- Safety and Regulatory Approvals
 - ◆ UL1577, 4,170 VAC_{RMS} for 1 Minute
 - ◆ DIN-EN/IEC60747-5-5, 850 V Peak Working Insulation Voltage
- These are Pb-Free Devices

Applications

- Power Supply Regulators
- Digital Logic Inputs
- Microprocessor Inputs

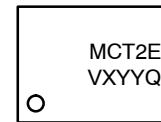


PDIP6 8.51x6.35, 2.54P
CASE 646BX



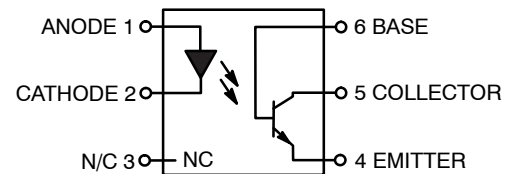
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MARKING DIAGRAM



- MCT2E = Device Number
 V = DIN EN/IEC60747-5-5 Option (only appears on component ordered with this option)
 X = One-Digit Year Code, e.g., '5'
 YY = Digit Work Week, Ranging from '01' to '53'
 Q = Assembly Package Code

SCHEMATIC



ORDERING INFORMATION

See detailed ordering and shipping information on page 7 of this data sheet.

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SAFETY AND INSULATION RATINGS (As per DIN EN/IEC 60747-5-5, this optocoupler is suitable for “safe electrical insulation” only within the safety limit data. Compliance with the safety ratings shall be ensured by means of protective circuits.)

Parameter		Characteristics
Installation Classifications per DIN VDE 0110/1.89 Table 1, For Rated Mains Voltage	<150 V _{RMS}	I-IV
	<300 V _{RMS}	I-IV
Climatic Classification		55/100/21
Pollution Degree (DIN VDE 0110/1.89)		2
Comparative Tracking Index		175

Symbol	Parameter	Value	Unit
V _{PR}	Input-to-Output Test Voltage, Method A, V _{IORM} × 1.6 = V _{PR} , Type and Sample Test with t _m = 10 s, Partial Discharge < 5 pC	1360	V _{peak}
	Input-to-Output Test Voltage, Method B, V _{IORM} × 1.875 = V _{PR} , 100% Production Test with t _m = 1 s, Partial Discharge < 5 pC	1594	V _{peak}
V _{IORM}	Maximum Working Insulation Voltage	850	V _{peak}
V _{IOTM}	Highest Allowable Over-Voltage	6000	V _{peak}
	External Creepage	≥7	mm
	External Clearance	≥7	mm
	External Clearance (for Option TV, 0.4" Lead Spacing)	≥10	mm
DTI	Distance Through Insulation (Insulation Thickness)	≥0.5	mm
T _S	Case Temperature (Note 1)	175	°C
I _{S, INPUT}	Input Current (Note 1)	350	mA
P _{S, OUTPUT}	Output Power (Note 1)	800	mW
R _{IO}	Insulation Resistance at T _S , V _{IO} = 500 V (Note 1)	>10 ⁹	Ω

1. Safety limit values – maximum values allowed in the event of a failure.

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ABSOLUTE MAXIMUM RATINGS ($T_A = 25^\circ\text{C}$ unless otherwise noted)

Symbol	Parameter	Device	Value	Unit
TOTAL DEVICE				
T_{STG}	Storage Temperature	All	-40 to +125	$^\circ\text{C}$
T_{OPR}	Operating Temperature	All	-40 to +100	$^\circ\text{C}$
T_J	Junction Temperature Range	All	-40 to +125	$^\circ\text{C}$
T_{SOL}	Lead Solder Temperature	All	260 for 10 seconds	$^\circ\text{C}$
P_D	Total Device Power Dissipation at $T_A = 25^\circ\text{C}$	All	250	mW
	Derate Above 25°C	All	2.94	$\text{mW}/^\circ\text{C}$
EMITTER				
I_F		All	60	mA
V_R	Reverse Input Voltage	TIL111M	3	V
		MCT2EM, TIL117M	6	V
$I_F(\text{pk})$	Forward Current – Peak (300 μs , 2% Duty Cycle)	All	3	A
P_D	LED Power Dissipation at $T_A = 25^\circ\text{C}$	All	120	mW
	Derate Above 25°C	All	1.41	$\text{mW}/^\circ\text{C}$
DETECTOR				
V_{CEO}	Collector-to-Emitter Voltage	All	30	V
V_{CBO}	Collector-to-Base Voltage	All	70	V
V_{ECO}	Emitter-to-Collector Voltage	All	7	V
V_{EBO}	Emitter-to-Base Voltage	All	7	V
P_D	Detector Power Dissipation at $T_A = 25^\circ\text{C}$	All	150	mW
	Derate Above 25°C	All	1.76	$\text{mW}/^\circ\text{C}$

Stresses exceeding those listed in the Maximum Ratings table may damage the device. If any of these limits are exceeded, device functionality should not be assumed, damage may occur and reliability may be affected.

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ELECTRICAL CHARACTERISTICS (T_A = 25°C, unless otherwise noted)

Symbol	Parameter	Test Conditions	Min	Typ	Max	Unit
INDIVIDUAL COMPONENT CHARACTERISTICS						
EMITTER						
V _F	Input Forward Voltage	I _F = 10 mA	–	1.18	1.50	V
I _R	Reverse Leakage Current	V _R = 6.0 V	–	0.001	10	μA
DETECTOR						
BV _{CEO}	Collector-to-Emitter Breakdown Voltage	I _C = 1.0 mA, I _F = 0	30	100	–	V
BV _{CBO}	Collector-to-Base Breakdown Voltage	I _C = 100 μA, I _F = 0	70	120	–	V
BV _{EBO}	Emitter-to-Base Breakdown Voltage	I _E = 10 μA, I _F = 0	7	10	–	V
BV _{ECO}	Emitter-to-Collector Breakdown Voltage	I _E = 100 μA, I _F = 0	7	10	–	V
I _{CEO}	Collector-to-Emitter Dark Current	V _{CE} = 10 V, I _F = 0	–	1	50	nA
I _{CBO}	Collector-to-Base Dark Current	V _{CB} = 10 V	–	–	20	nA
C _{CE}	Capacitance	V _{CE} = 0 V, f = 1 MHz	–	8	–	pF

ELECTRICAL CHARACTERISTICS (T_A = 25°C, unless otherwise noted) (continued)

Symbol	Parameter	Test Conditions	Device	Min	Typ	Max	Unit
TRANSFER CHARACTERISTICS							
DC CHARACTERISTICS							
CTR	Current Transfer Ratio, Collector-to-Emitter	I _F = 10 mA, V _{CE} = 10 V	MCT2EM	20	–	–	%
			TIL117M	50	–	–	
V _{CE(SAT)}	Collector-to-Emitter Saturation Voltage	I _C = 2 mA, I _F = 16 mA	MCT2EM, TIL111M	–	–	0.4	V
		I _C = 0.5 mA, I _F = 10 mA	TIL117M	–	–	0.4	
AC CHARACTERISTICS							
T _{ON}	Non-Saturated Turn-on Time	I _F = 10 mA, V _{CC} = 10 V, R _L = 100 Ω (Figure 11)	MCT2EM	–	2	–	μs
		I _C = 2 mA, V _{CC} = 10 V, R _L = 100 Ω (Figure 11)	TIL117M	–	2	10	
T _{OFF}	Turn-off Time	I _F = 10 mA, V _{CC} = 10 V, R _L = 100 Ω (Figure 11)	MCT2EM	–	2	–	μs
		I _F = 2 mA, V _{CC} = 10 V, R _L = 100 Ω (Figure 11)	TIL117M	–	2	10	

ELECTRICAL CHARACTERISTICS (T_A = 25°C, unless otherwise noted) (continued)

Symbol	Parameter	Test Conditions	Min	Typ	Max	Unit
ISOLATION CHARACTERISTICS						
V _{ISO}	Input-Output Isolation Voltage	t = 1 Minute	4170	–	–	V _{ACRMS}
C _{ISO}	Isolation Capacitance	V _{I-O} = 0 V, f = 1 MHz	–	0.2	–	pF
R _{ISO}	Isolation Resistance	V _{I-O} = ±500 VDC, T _A = 25°C	10 ¹¹	–	–	Ω

Product parametric performance is indicated in the Electrical Characteristics for the listed test conditions, unless otherwise noted. Product performance may not be indicated by the Electrical Characteristics if operated under different conditions.

TYPICAL PERFORMANCE CURVES

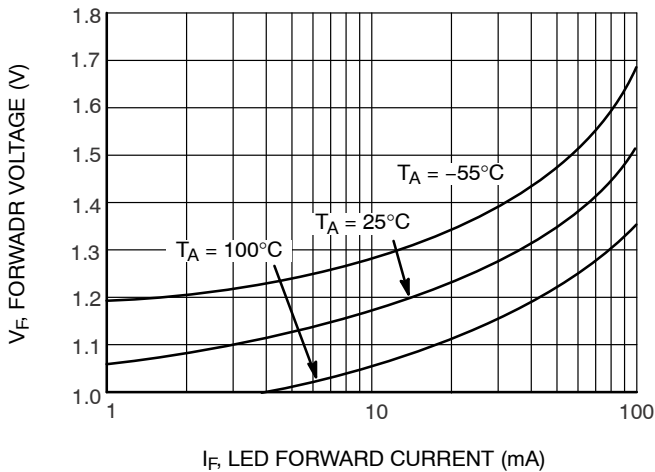


Figure 1. LED Forward Voltage vs. Forward Current

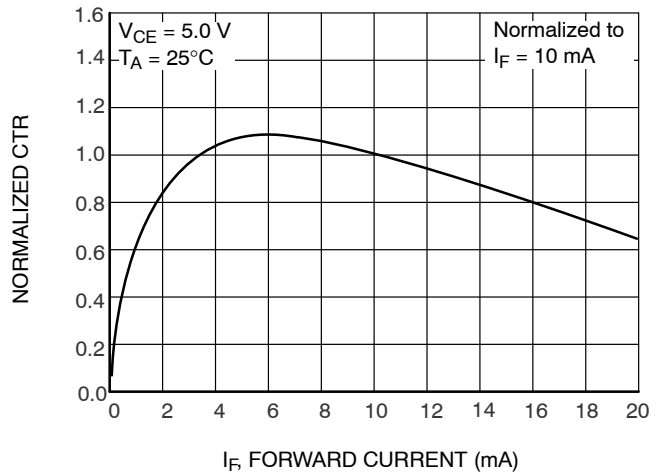


Figure 2. Normalized CTR vs. Forward Current

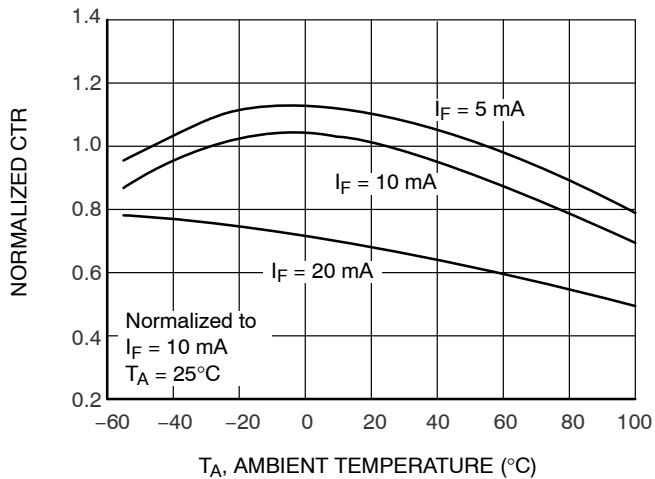


Figure 3. Normalized CTR vs. Ambient Temperature

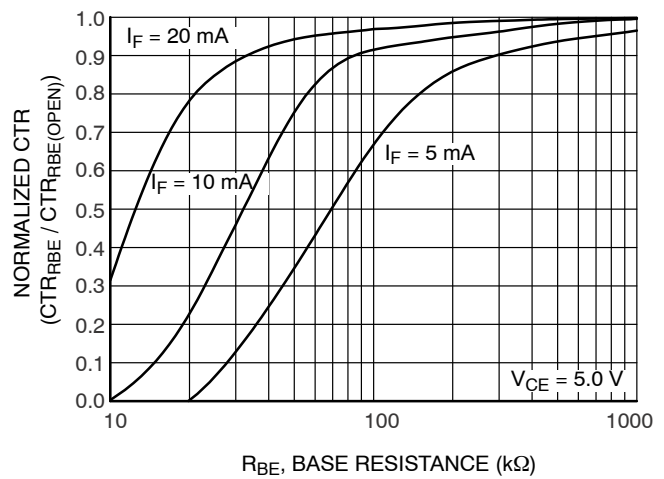


Figure 4. CTR vs. R_{BE} (Unsaturated)

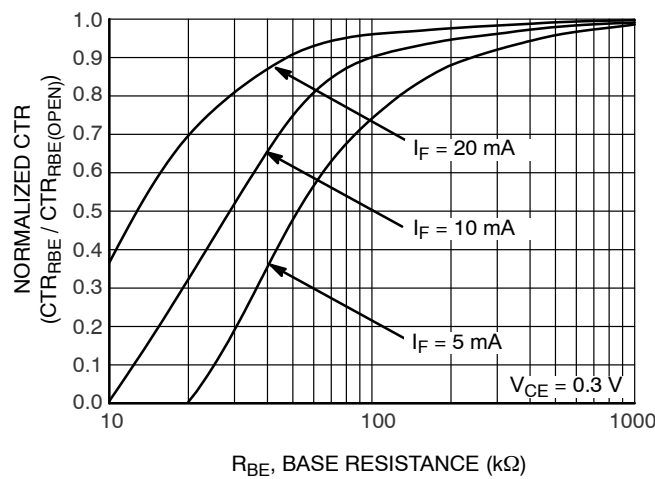


Figure 5. CTR vs. R_{BE} (Saturated)

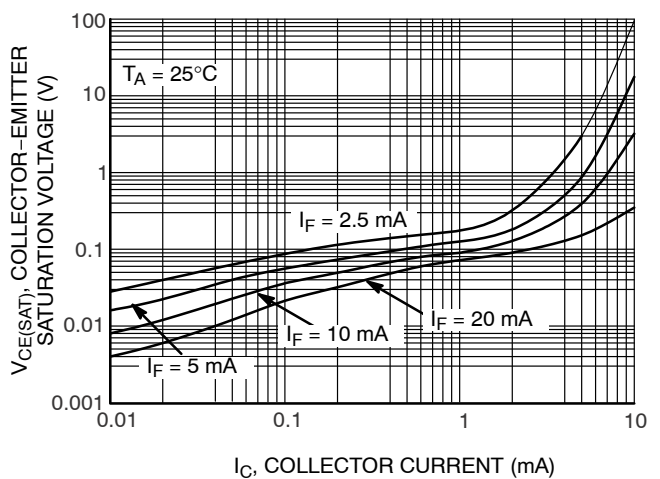


Figure 6. Collector-Emitter Saturation Voltage vs. Collector Current

TYPICAL PERFORMANCE CURVES (continued)

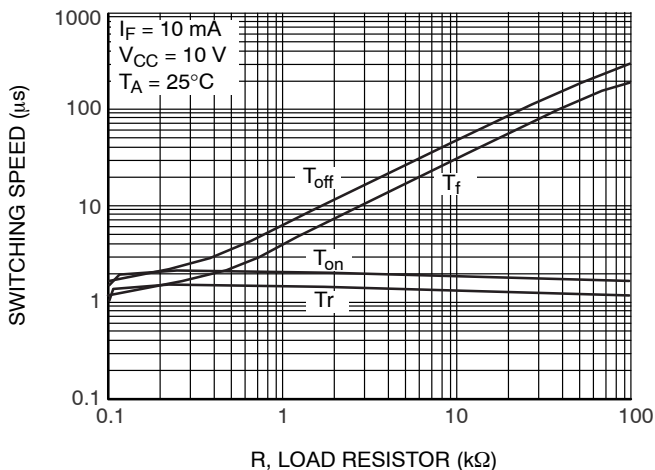


Figure 7. Switching Speed vs. Load Resistor

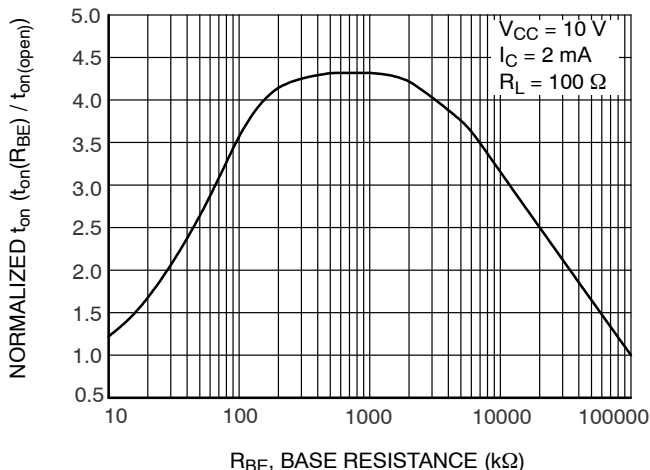


Figure 8. Normalized t_{on} vs. R_{BE}

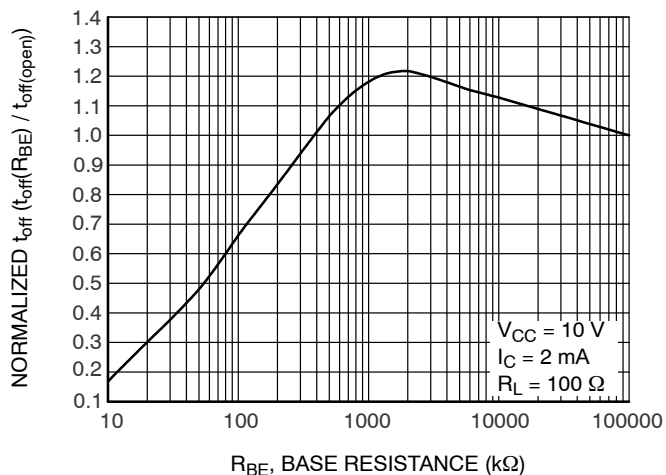


Figure 9. Normalized t_{off} vs. R_{BE}

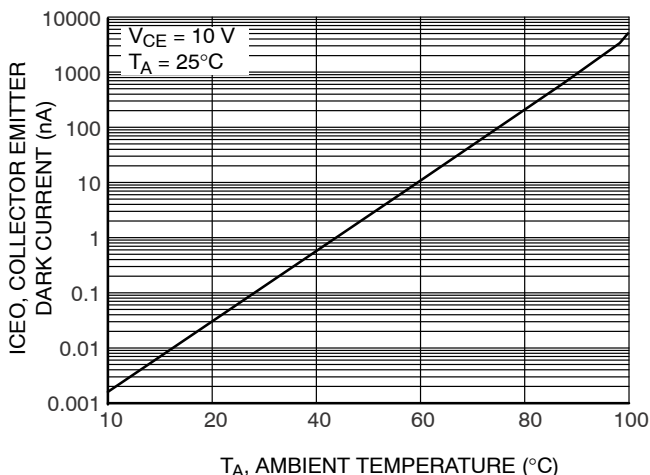


Figure 10. Dark Current vs. Ambient Temperature

SWITCHING TIME TEST CIRCUIT AND WAVEFORMS

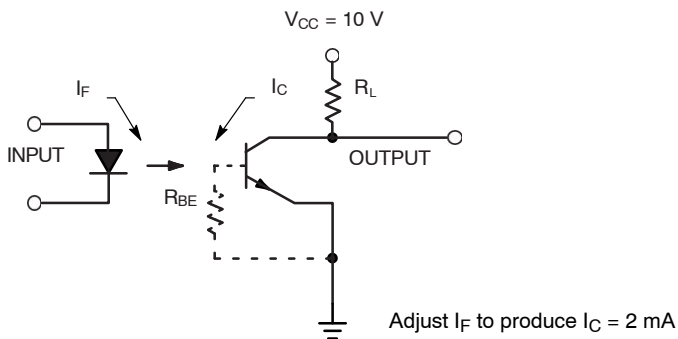


Figure 11. Switching Time Test Circuit and Waveforms

MCT2EM, TIL111M, TIL117M

REFLOW PROFILE

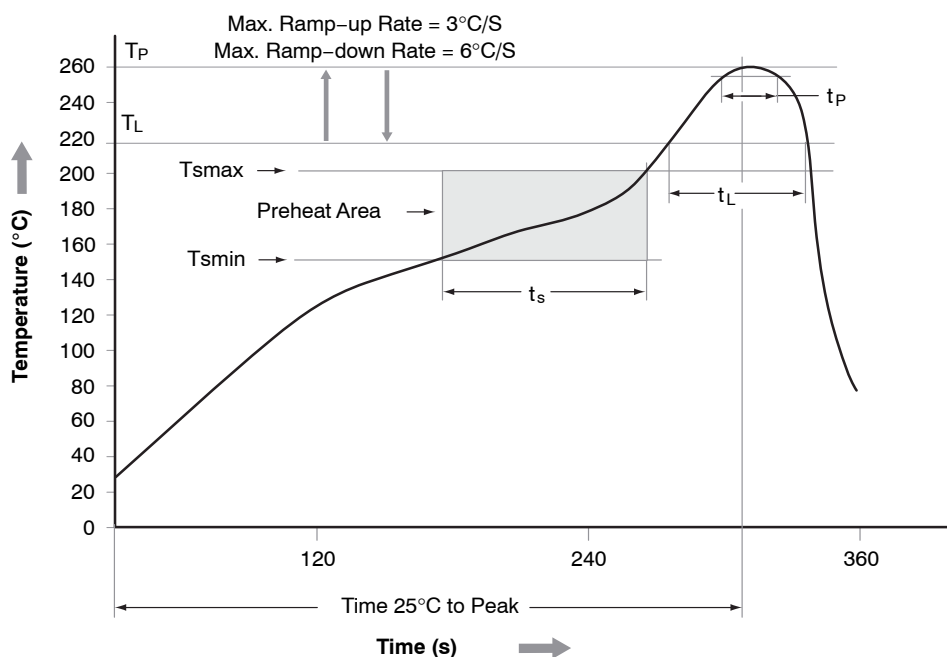


Figure 12. Reflow Profile

Table 1.

Profile Feature	Pb-Free Assembly Profile
Temperature Minimum (T _{min})	150°C
Temperature Maximum (T _{max})	200°C
Time (t _s) from (T _{min} to T _{max})	60 – 120 seconds
Ramp-up Rate (t _L to t _p)	3°C/second maximum
Liquidous Temperature (T _L)	217°C
Time (t _L) Maintained Above (T _L)	60 – 150 seconds
Peak Body Package Temperature	260°C +0°C / -5°C
Time (t _p) within 5°C of 260°C	30 seconds
Ramp-down Rate (T _P to T _L)	6°C/second maximum
Time 25°C to Peak Temperature	8 minutes maximum

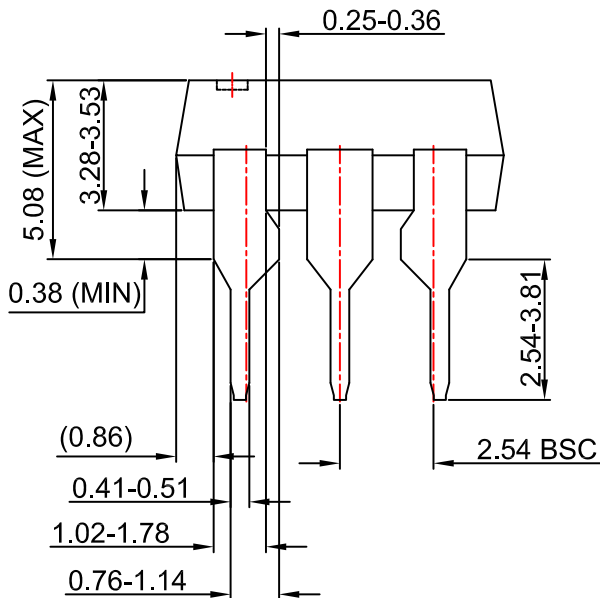
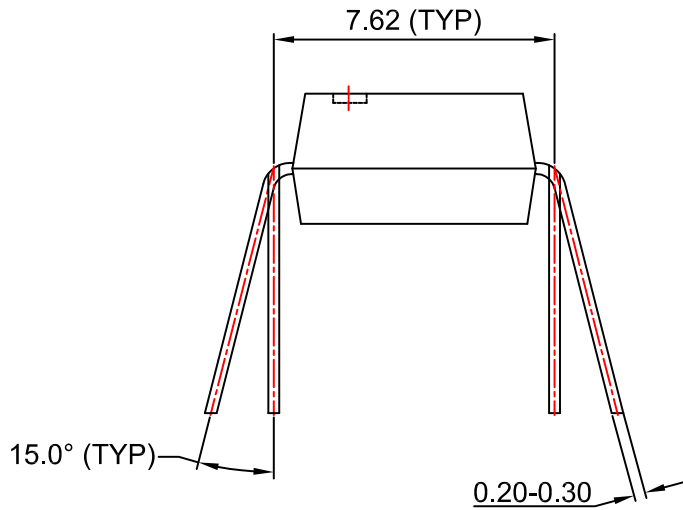
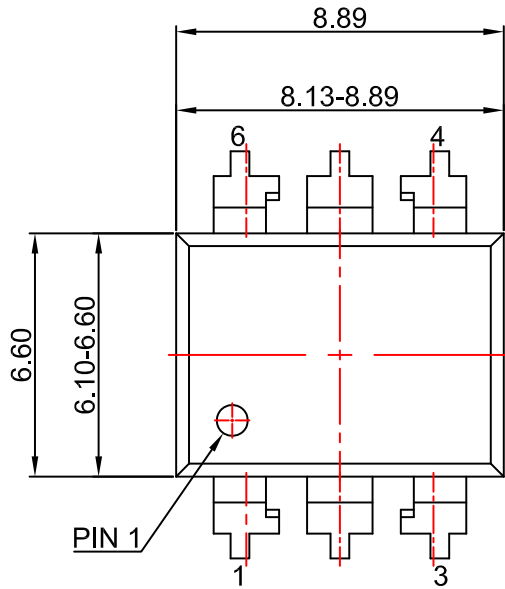
ORDERING INFORMATION (Note 2)

Part Number	Package	Packing Method
MCT2EM	DIP 6-Pin	Tube (50 Units)
MCT2ESM	SMT 6-Pin (Lead Bend)	Tube (50 Units)
MCT2ESR2M	SMT 6-Pin (Lead Bend)	Tape and Reel (1000 Units)
MCT2EVM	DIP 6-Pin, DIN EN/IEC60747-5-5 Option	Tube (50 Units)
MCT2ESVM	SMT 6-Pin (Lead Bend), DIN EN/IEC60747-5-5 Option	Tube (50 Units)
MCT2ESR2VM	SMT 6-Pin (Lead Bend), DIN EN/IEC60747-5-5 Option	Tape and Reel (1000 Units)
MCT2ETVM	DIP 6-Pin, 0.4" Lead Spacing, DIN EN/IEC60747-5-5 Option	Tube (50 Units)

2. The product orderable part number system listed in this table also applies to the TIL111M and TIL117M devices.

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CASE 646BX
ISSUE O

DATE 31 JUL 2016



NOTES:

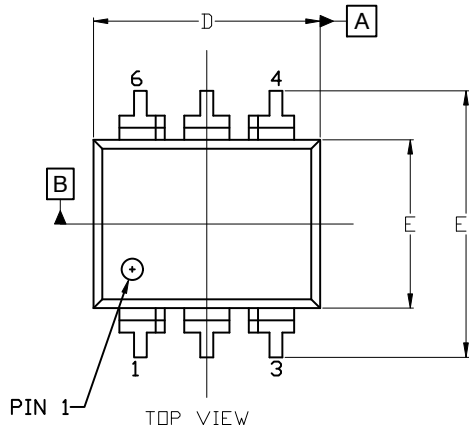
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CASE 646BY
ISSUE A

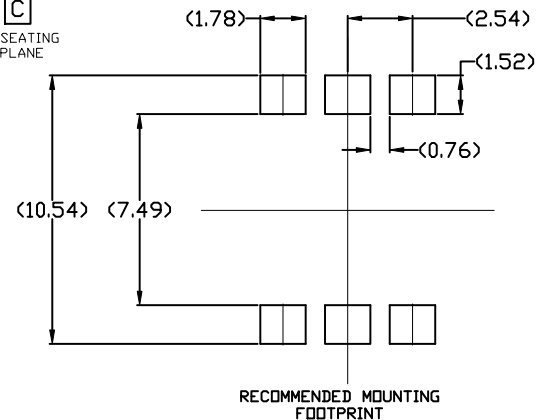
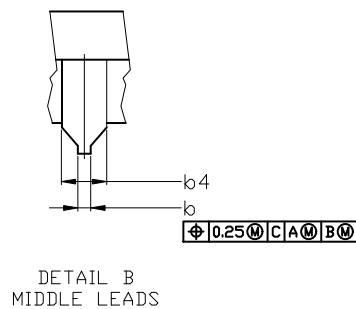
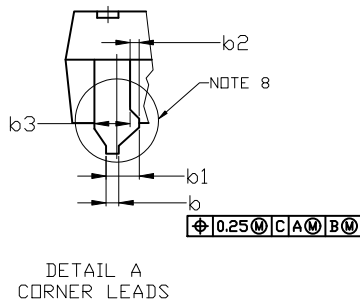
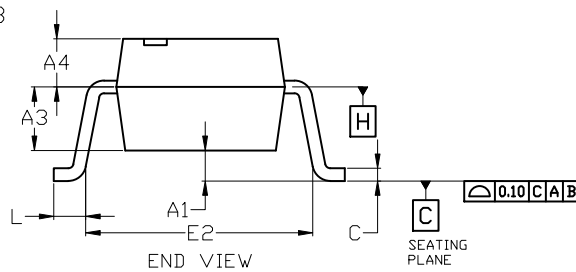
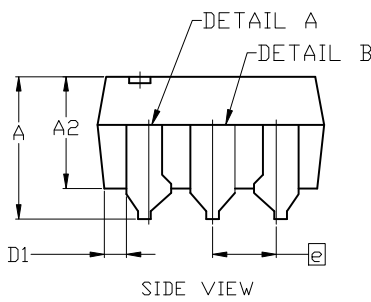
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NOTES:

1. DIMENSIONING AND TOLERANCING PER ASME Y14.5M, 2009.
2. CONTROLLING DIMENSION: MILLIMETERS
3. DIMENSIONS A, A1, AND L ARE MEASURED WITH THE PACKAGE SEATED.
4. DIMENSIONS D, D1, AND E1 DO NOT INCLUDE MOLD FLASH OR PROTRUSIONS. MOLD FLASH OR PROTRUSIONS ARE NOT TO EXCEED 2.54mm.
5. PACKAGE CONTOUR IS OPTIONAL (ROUNDED OR SQUARE CORNERS).
6. CENTER LINE OF CORNER LEADS ARE LOCATED BY LOCATING THE CENTER OF FEATURE b2 AND b3.

DIM	MILLIMETERS		
	MIN.	NOM.	MAX.
A	---	---	4.80
A1	0.38	---	---
A2	3.28	3.40	3.53
A3	2.49 REF		
A4	1.89 REF		
b	0.41	0.46	0.51
b1	0.76	0.92	1.14
b2	0.25	0.28	0.36
b3	1.02	1.40	1.78
b4	1.778 REF		
c	0.20	0.25	0.30
D	8.13	8.51	8.89
D1	0.86 REF		
E	6.10	6.35	6.60
E1	8.43	9.17	9.90
E2	8.13 REF		
e	2.54 BSC		
L	0.16	0.52	0.88



For additional information on our Pb-Free strategy and soldering details, please download the DN Semiconductor Soldering and Mounting Techniques Reference Manual, SLDERRM/D.

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